



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Jiping Li and Peter G. Borden
Assignee: Applied Materials, Inc.
Title: Evaluating Sidewall Coverage In A Semiconductor Wafer
Serial No.: 09/788,273 Filing Date: Feb. 16, 2001
Examiner: Michelle Estrada Group Art Unit: 2823
Docket No.: BOX009 US Confirmation No: 6335

Santa Clara, California
January 14, 2004

Mail Stop RCE
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to Final Office Action dated October 15, 2003, please amend the above-identified application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.

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